

Notice of References Cited	Application/Control No. 10/639,942	Applicant(s)/Patent Under Reexamination ROBERT H. DENNARD	
	Examiner Stanetta D. Isaac	Art Unit 2812	Page 1 of 1

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*	B	US-5,273,921	12-1993	Neudeck et al.	438/157
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Stanley Wolf and Richard N. Tauber, Silicon Processing For The VLSI Era, 1986 Lattice Press, Vol. I, pages 303-308
*	V	Stanley Wolf, Silicon Processing For The VLSI Era, 1990, Lattice Press, Vol. II, pages 45-47 and 144-146
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.